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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/944,426	08/30/2001	Vladislav Vashchenko	75292/13356	1844
7590 04/01/2004		EXAMINER		
Jurgen K Vollrath			NADAV, ORI	
588 Sutter Street #531 San Francisco, CA 94102			ART UNIT	PAPER NUMBER
,			2811	
			DATE MAILED: 04/01/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)				
	09/944,426	VASHCHENKO, VLADISL	AV			
Office Action Summary	Examiner	Art Unit	<del> </del>			
	ori nadav	2811				
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address				
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be time within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	ely filed s will be considered timely. the mailing date of this communicati D (35 U.S.C. § 133).	on.			
Status						
1) Responsive to communication(s) filed on 12 Ja	nuary 2004.					
,—	∑ This action is FINAL. 2b)  This action is non-final.					
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is						
closed in accordance with the practice under E	x parte Quayle, 1935 C.D. 11, 45	53 O.G. 213.				
Disposition of Claims		•				
4) Claim(s) 1-6 is/are pending in the application.						
4a) Of the above claim(s) <u>1</u> is/are withdrawn from consideration.						
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>2-6</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and/or	r election requirement.					
Application Papers						
9) The specification is objected to by the Examine	r.					
10) The drawing(s) filed on is/are: a) acce		Examiner.				
Applicant may not request that any objection to the						
Replacement drawing sheet(s) including the correct	ion is required if the drawing(s) is obj	ected to. See 37 CFR 1.121	(d).			
11) The oath or declaration is objected to by the Ex	aminer. Note the attached Office	Action or form PTO-152.				
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign	priority under 35 U.S.C. § 119(a)	-(d) or (f).				
a) ☐ All b) ☐ Some * c) ☐ None of:						
1. Certified copies of the priority documents	s have been received.					
2. Certified copies of the priority documents						
3. Copies of the certified copies of the prior	ity documents have been receive	ed in this National Stage				
application from the International Bureau						
* See the attached detailed Office action for a list	of the certified copies not receive	ed.				
Attachment(s)		,				
Notice of References Cited (PTO-892)	4) Interview Summary Paper No(s)/Mail Da					
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)  B) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)		atent Application (PTO-152)				
Paper No(s)/Mail Date	6)  Other:					

Application/Control Number: 09/944,426

Art Unit: 2811

#### **DETAILED ACTION**

## Claim Objections

Claim 2 is objected to because of the following informalities: The phrase "an n+ and", in line 2, should read "an n+ region and". The phrase "a n+ region" in line 3 should read "an n+ region".

Appropriate correction is required.

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 2-6 are rejected under 35 U.S.C. 102(e) as being anticipated by Ker et al. (6,465,848).

Regarding claim 2, Ker et al. teach in figure 3a and related text a method of increasing the holding voltage of a LVTSCR structure that includes an n-well 42 and a p-well 44 formed in a substrate 40, an n+ 46 and a p+ region 48 formed in the n-well 42, and an n+ region 54 formed in the p-well 44, the method comprising forming at least one p+

Application/Control Number: 09/944,426

Art Unit: 2811

region 58 and at least one additional n+ region 60 inside the p-well 44 of the structure to define at least one p-n junction in the p well that is forward biased during normal operation.

Regarding claims 3-6, Ker et al. teach in figure 3a and related text a method of increasing the holding voltage of a LVTSCR structure having an anode in an n-well 42 and a cathode in a p- well 44, comprising providing an alternative current path from anode to cathode through the p-well of the structure, other than purely the current path from anode to cathode through the p-material of the p-well, wherein the alternative current path defines a lower resistance current path than the p-well, wherein the lower resistance current path takes the form of at least one p-n junction that is forward biased under normal operating conditions, formed in the p-well, and wherein at least one diode is formed in the p-well which provides a low resistance current path through the at least one diode once the threshold voltage across the at least one diode is exceeded.

## Response to Arguments

Applicant's arguments with respect to claims 2-6 have been considered but are most in view of the new ground(s) of rejection.

Art Unit: 2811

#### **Conclusion**

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. References B-C are cited as being related to LVTSCR and twin well structures, respectively.

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Papers related to this application may be submitted to Technology center (TC) 2800 by facsimile transmission. Papers should be faxed to TC 2800 via the TC 2800 Fax center located in Crystal Plaza 4, room 4-C23. The faxing of such papers must conform with the notice published in the Official Gazette, 1096 OG 30 (November 15, 1989). The Group 2811 Fax Center number is (703) 308-7722 and 308-7724. The Group 2811 Fax Center is to be used only for papers related to Group 2811 applications.

Any inquiry concerning this communication or any earlier communication from the Examiner should be directed to *Examiner Nadav* whose telephone number is **(571) 272-1660**. The Examiner is in the Office generally between the hours of 7 AM to 4 PM (Eastern Standard Time) Monday through Friday.

Any inquiry of a general nature or relating to the status of this application should be directed to the **Technology Center Receptionists** whose telephone number is **308-0956**.

O.N. March 29, 2004 ORI NADAV
PATENT EXAMINER
TECHNOLOGY CENTER 2800

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